

CLAIMS

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1. A shared memory comprising:
a plurality of multi-port memories each having at least one port with a
copybus- function, and at least one port accessible from user side; and
5 at least one copybus to be connected to said at least one port with the
copybus-function;
wherein said shared memory is adapted to copy contents of one of said
multi-port memories, which has been changed by a writing operation from
said user side, to other multi-port memories through said at least one copybus.
- 10 2. The shared memory according to claim 1, wherein said contents are
adapted to be copied optically.
3. The shared memory according to claim 1 or 2, wherein said shared
memory is formed by an integrated circuit technique.
4. The shared memory according to claim 3, wherein said integrated
15 circuit technique is a two-dimensional integrated circuit technique.
5. The shared memory according to claim 3, wherein said integrated
circuit technique is a three-dimensional integrated circuit technique.

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